

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES  
SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865 Filed: December 5, 2003

Examiner: Not Yet Assigned Group Art Unit: 2818

NOA Date: N/A Issue Batch: N/A

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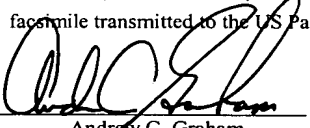
March 30, 2004

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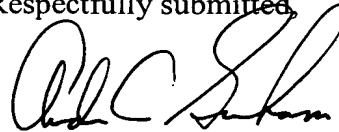
**SUBMISSION OF NEW FORMAL DRAWINGS**

Transmitted herewith are 17 sheets of new drawings containing Fig. 1; Fig. 2; Fig. 3; Fig. 4 and Fig. 5; Fig. 6 and Fig. 7; Fig. 8; Fig. 9; Fig. 10; Fig. 11; Fig. 12; Fig. 13; Fig. 14; Fig. 15; Fig. 16; Fig. 17A, Fig. 17B and Fig. 17C; Fig. 17D and Fig. 17E; and Fig. 18, respectively.

These formal drawings are being submitted prior to the first Office Action on the merits to facilitate the Examiner's review, and in hopes that the Official Draftsperson will review these formal drawings rather than the informal drawings submitted with the application as filed.

<b>CERTIFICATE OF MAILING OR TRANSMISSION</b>	
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 Andrew C. Graham	<u>3-30-04</u> Date

Respectfully submitted,



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